

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): YAMAGUCHI	Atty. Dkt.: 01-116-DIV
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: SEMICONDUCTOR DEVICE INCLUDING POWER MOSFET AND PERIPHERAL DEVICE AND METHOD FOR MANUFACTURING THE SAME	

Commissioner for Patents
Arlington, VA 22202

Date: July 18, 2003

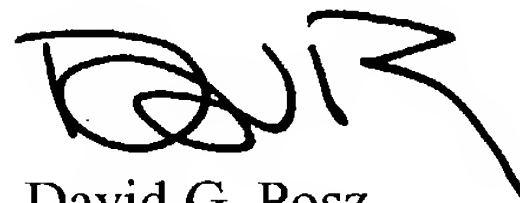
INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are being submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,



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FORM PTO-1449	ATTY. DKT NO.	1-116-DIV	SER. NO.
	APPLICANT	YAMAGUCHI	
	FILING DATE	July 18, 2003	GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	✓	5,828,101	Oct. 27, 1998	Endo		
	✓	6,525,375	Feb. 25, 2003	Yamaguchi et al.		
	✓	5,233,215	Aug. 3, 1993	Baliga		
	✓	6,191,447	Feb. 20, 2001	Baliga		
	✓	5,929,482	Jul 27, 1999	Kawakami et al.		
	✓	5,831,338	Nov. 3, 1998	Kawamoto		
	✓	6,236,099	May 22, 2001	Boden, Jr.		
	✓	4,896,199	Jan. 23, 1990	Tsuzuki et al.		

FOREIGN PATENT DOCUMENTS

								TRANSLATION	
		DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS	YES	NO
	✓	61-125174	6/12/86	JAPAN				X	
	✓	08-204195	8/9/96	JAPAN				X	
	✓	10-214969	8/11/98	JAPAN				X	
	✓	11-103058	4/13/99	JAPAN				X	
	✓	11-150265	6/2/99	JAPAN				X	

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

EXAMINER	DATE CONSIDERED	